

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electro-optical device, comprising:

an active matrix substrate having on the same plane a plurality of scanning lines, a plurality of signal lines provided to intersect the scanning lines, a plurality of pixel electrodes provided at the intersection portions of the scanning lines and the signal lines, and a peripheral driving circuit to matrix drive the pixel electrodes, and wiring lines for supplying signals to the peripheral driving circuit, the peripheral driving circuit including a sample hold circuit with thin film transistors each having a channel region;

a counter substrate comprising:

a common electrode facing the pixel electrodes of the active matrix, and

a light shielding film;

a seal that forms a sealed region between the active matrix substrate and the counter substrate, the sample hold circuit of the peripheral driving circuit being disposed at least partially completely within the sealed region; region and the wiring being disposed at least partially within the seal; and

a liquid crystal layer disposed in the sealed region between the active matrix substrate and the counter substrate,

wherein both the common electrode and the light shielding film are in a non-overlapping arrangement with at least one of both the peripheral driving circuit and the wiring lines for supplying signals to the peripheral driving circuit in plan view.

2. (Currently Amended) An electro-optical device, comprising:

an active matrix substrate having on the same plane a plurality of scanning lines, a plurality of signal lines provided to intersect the scanning lines, a plurality of pixel electrodes provided at the intersection portions of the scanning lines and the signal lines, and a peripheral driving circuit to matrix drive the pixel electrodes, and wiring lines for supplying signals to the peripheral driving circuit, the peripheral driving circuit including a sample hold circuit with thin film transistors each having a channel region;

a counter substrate comprising:

a common electrode covering a surface of the counter substrate, the common electrode facing the pixel electrodes of the active matrix, and

a light shielding film;

a seal that forms a sealed region between the active matrix substrate and the counter substrate, the sample hold circuit of the peripheral driving circuit being disposed at least partially completely within the sealed region; region and the wiring lines being disposed at least partially within the seal; and

a liquid crystal layer disposed in the sealed portion between the active matrix substrate and the counter substrate,

wherein both the counter substrate and the light shielding film are in a non-overlapping arrangement with at least one of both the peripheral driving circuit and the wiring lines for supplying signals to the peripheral driving circuit in plan view.

3. (Previously Presented) The electro-optical device according to claim 1, wherein the peripheral driving circuit comprises thin film transistors having channel regions made of single crystal silicon.

4. (Previously Presented) The electro-optical device according to claim 1, wherein a frequency of at least one of the signals supplied to the peripheral driving circuit is equal to or more than 10 MHz.

5. (Previously Presented) The electro-optical device according to claim 1, wherein the peripheral driving circuit comprises at least one of a data line driving circuit and a sample hold circuit, and the wiring lines comprise at least one of clock signal lines, image signal selecting lines, and image signal lines.

6. (Currently Amended) A method of manufacturing an electro-optical device, comprising:

forming a plurality of pixel electrodes and electrode, a peripheral driving circuit to matrix drive the plurality of pixel electrodes and wiring lines for supplying signals to the peripheral driving circuit on one surface of an active matrix substrate, the peripheral driving circuit including a sample hold circuit with thin film transistors each having a channel region;

forming a common electrode and a light shielding film on a surface of a counter substrate;

arranging both the common electrode and the light shielding film of the counter substrate in a non-overlapping arrangement ~~with at least one of both~~ the peripheral driving circuit and the wiring lines for supplying signals to the peripheral driving circuit in plan view;

bonding the active matrix substrate to the counter substrate with a predetermined gap between the active matrix substrate and the counter substrate using a sealing material to form a sealed region, the sample hold circuit of the peripheral driving circuit being disposed partially completely within the sealed region, region and the wiring being disposed at least partially within the seal, and the common electrode facing the pixel electrodes of the active matrix; and

forming a liquid crystal layer by injecting liquid crystal into the sealed region formed by the active matrix substrate, the counter substrate, and the sealing material.

7. (Original) An electronic apparatus, comprising:
the electro-optical device according to claim 1.